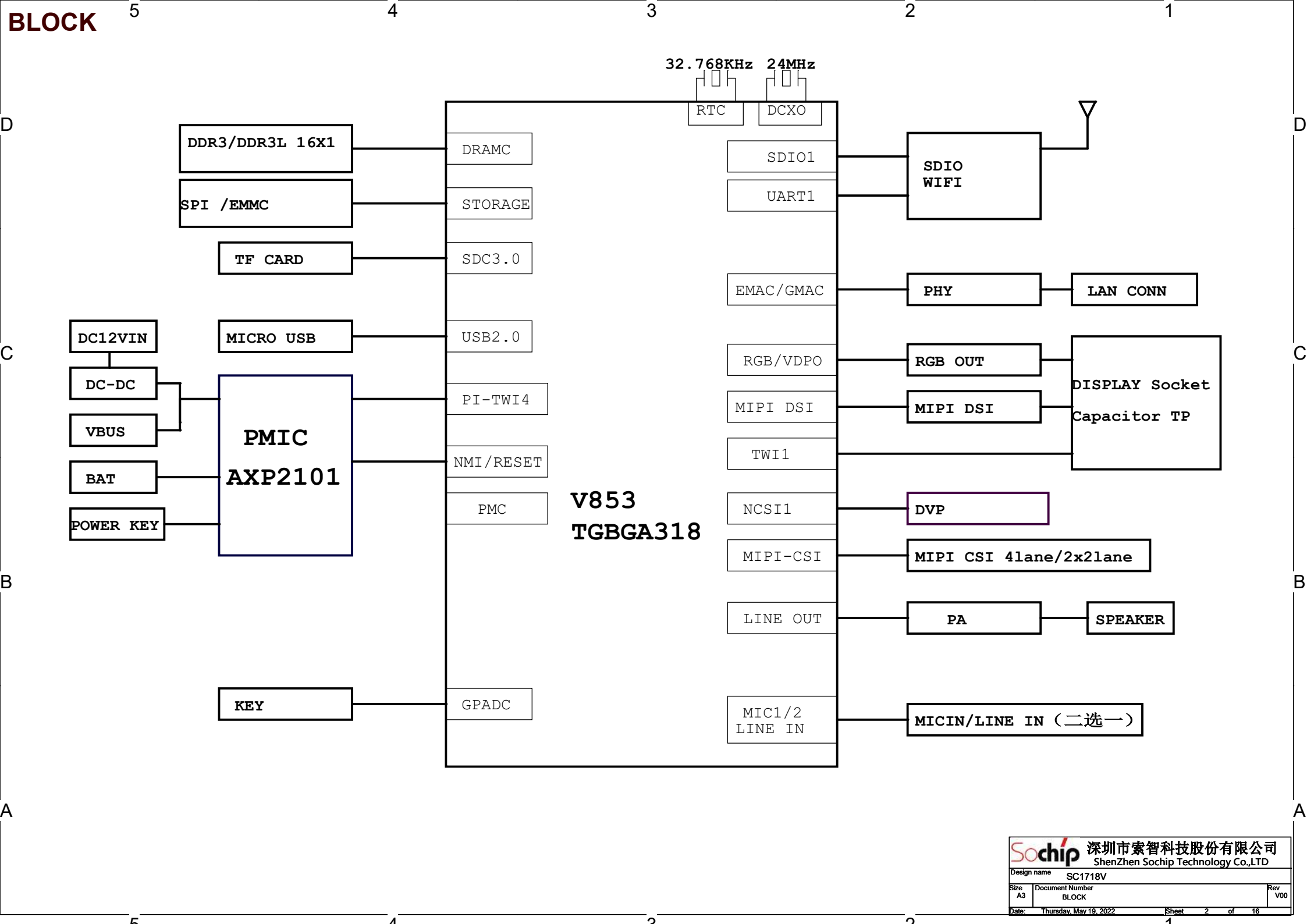


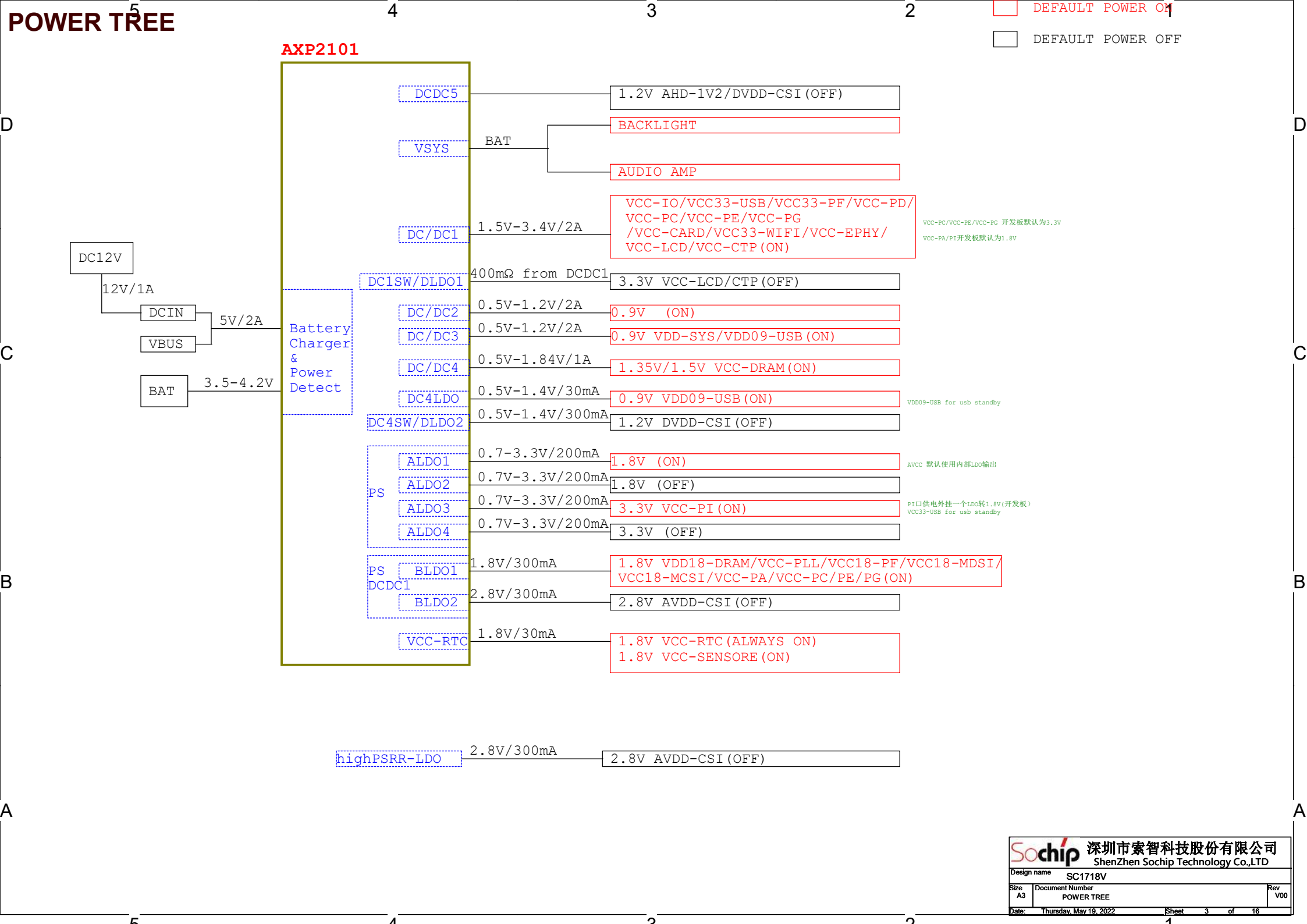
REVISION⁵ HISTORY

Schematics Index:

```
P01: REVISION HISTORY
P02: BLOCK
P03: POWER TREE
P04: GPIO ASSIGNMENT
P05: POWER
P06: CPU
P07: PF/PG/PH/PI
P08: DDR3_16X1
P09: eMMC/JTAG
P10: CARD/JTAG/UART
P11: AUDIO/KEY/ADC
P12: LCD/CTP/DSI
P13: MIPI CSI/PI
P14: CSI/RGMII/RMII
P15: SENSOR/USB
P16: WIFI
```

[illegible]





Sochip

深圳市索智科技股份有限公司

ShenZhen Sochip Technology Co.,LTD

Design name

SC1718V

Size

A3

Document Number

POWER TREE

Rev

V00

Date:

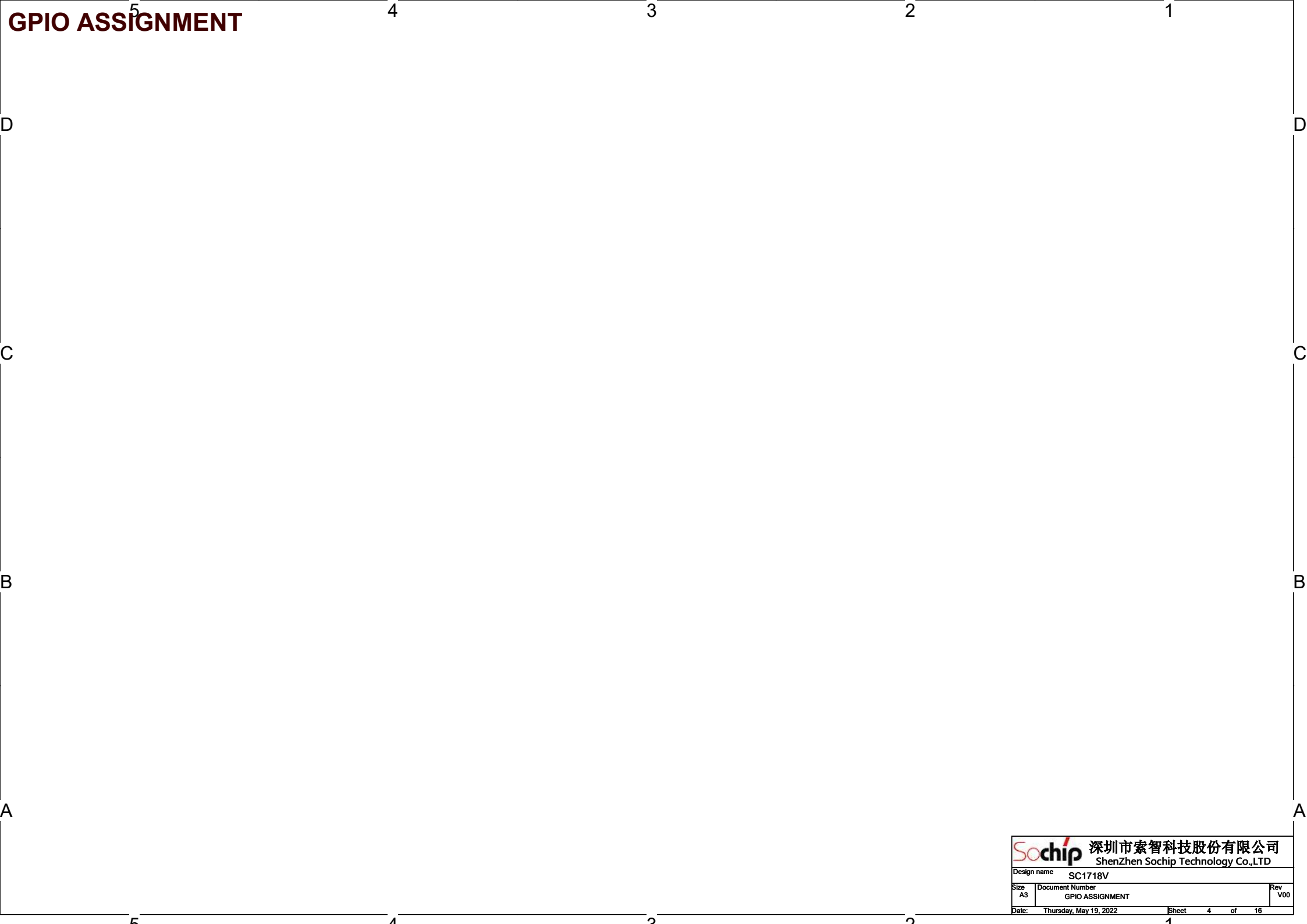
Thursday, May 19, 2022

Sheet

3

of

16



GPIO ASSIGNMENT

D

C

B

A

D

C

B

A

5

4

3

2

1

5

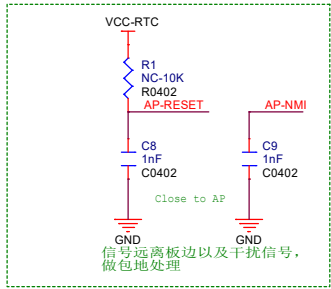
4

3

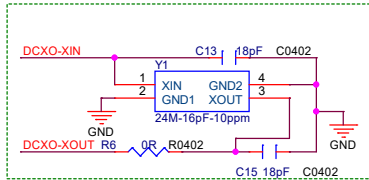
2

1

 深圳市索智科技股份有限公司 ShenZhen Sochip Technology Co.,LTD		
Design name SC1718V		
Size A3	Document Number GPIO ASSIGNMENT	Rev V00
Date: Thursday, May 19, 2022	Sheet 4	of 16

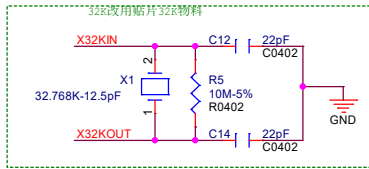


DCXO-24Mhz

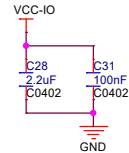
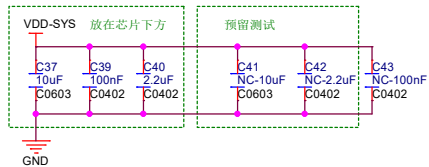


使用全志物料24Mhz=E3SB24E004304E

32.768KHz



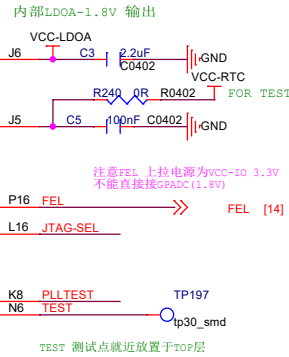
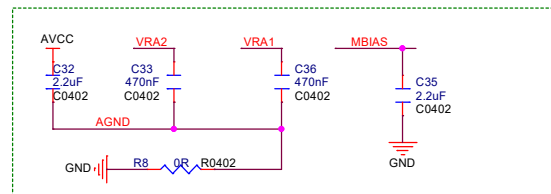
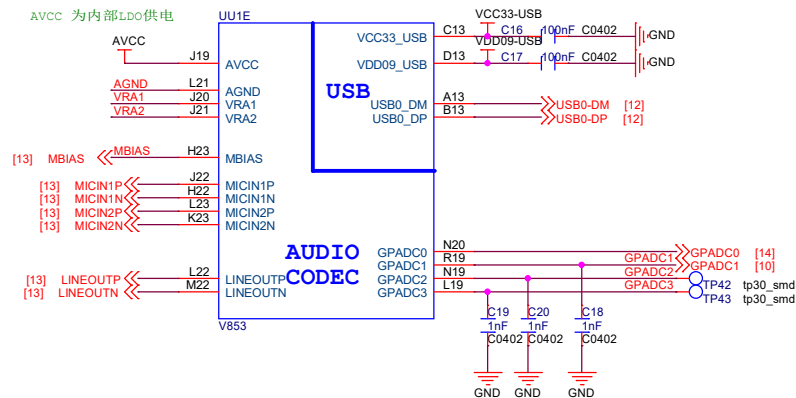
DECOUPLE CAP



PMC 模块功能由RESET-BYP决定，配置说明如下：

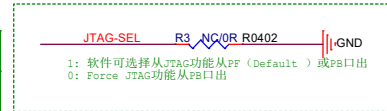
RESET-BYP	浮空为高电平	1. PMC 功能PIN有效； 2. 上电内部复位有效（需要外接高电平）； 3. 工作时支持外部复位PMC来复位SOC；
	接地为低电平	1. PMC 功能PIN无效；只有SW/RESET信号有效； 2. 上电内部复位有效；也可外接PMU_PWROK信号复位； 3. 工作时支持外部复位；

AVCC 为内部LDO供电

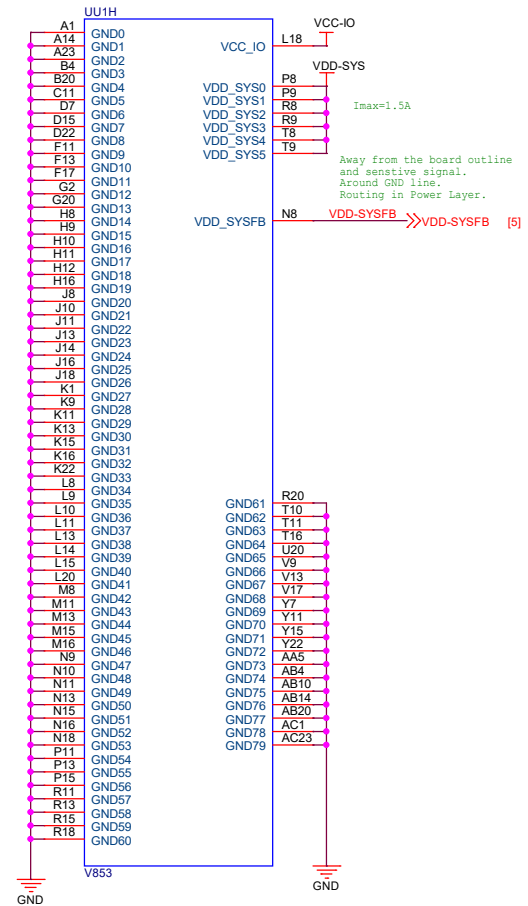
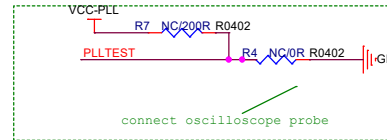


JTAG-SEL

PCB上注意用大字体标注，放置于板边

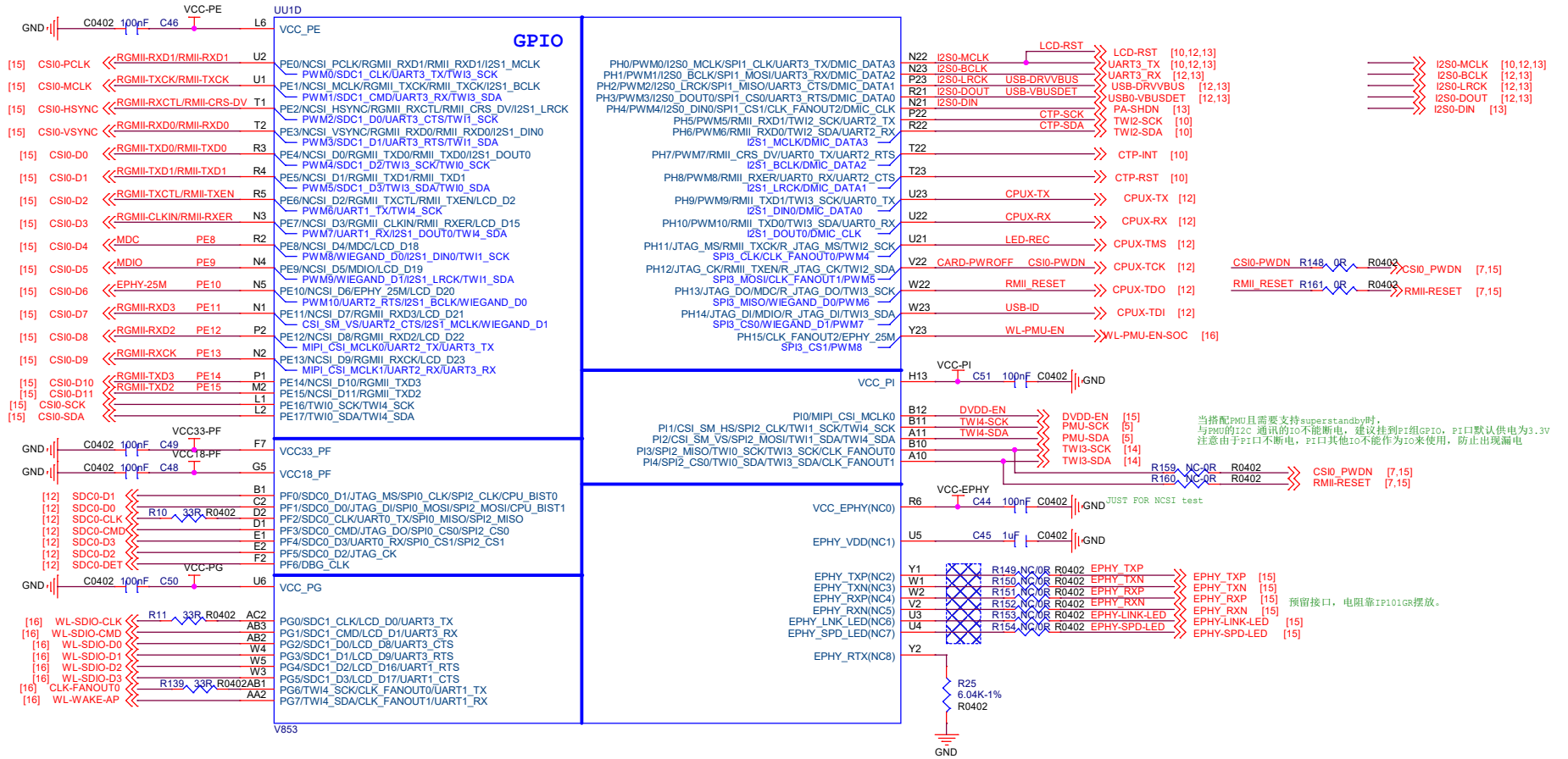


PLL-TEST



SOC

PE

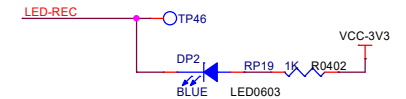


PE

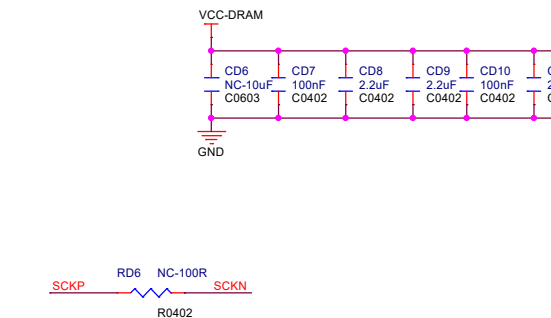
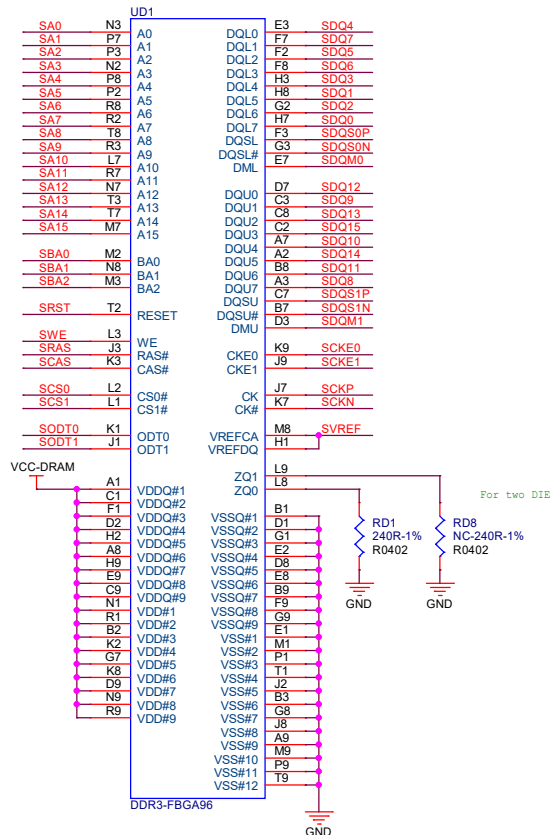
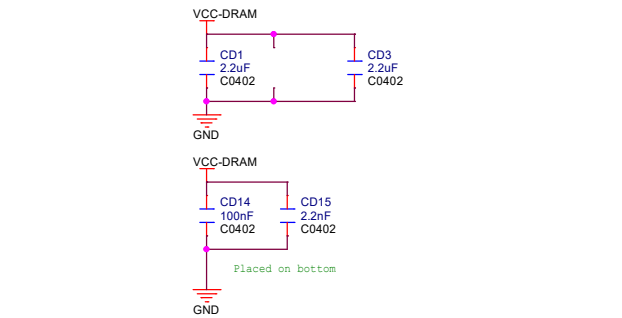
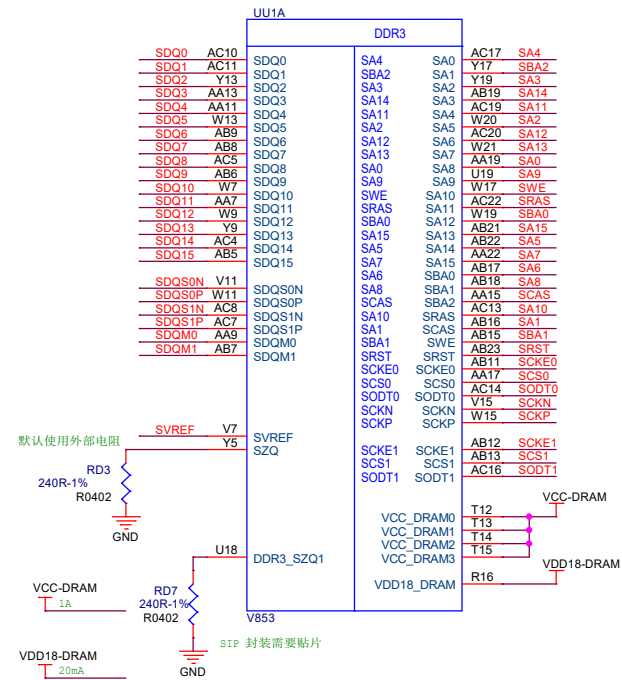
0R 电阻默认贴上,验证时可将其其他模块电阻去掉

[16] BT-RST-N-SOC	R105 0R R0402	PE8
[16] BT-UART-CTS	R106 0R R0402	PE10
[16] BT-UART-RTS	R107 0R R0402	PE11
[16] BT-UART-RX	R108 0R R0402	PE12
[16] BT-UART-TX	R109 0R R0402	PE13
[16] BT-WAKE-AP-SOC	R110 0R R0402	PE14
[16] AP-WAKE-BT-SOC	R111 0R R0402	PE15

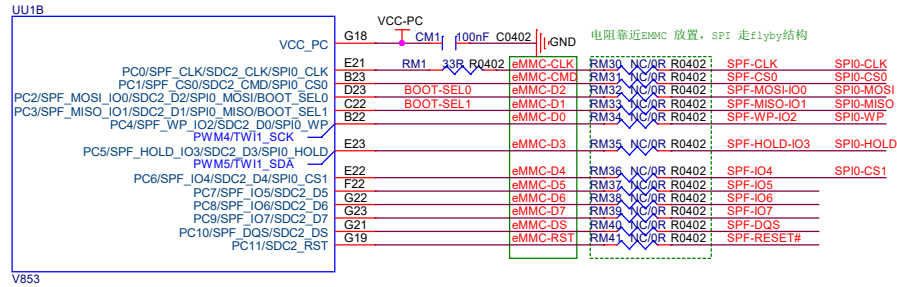
串口注意反向
蓝牙BT和RGMII/DVP不能同时使用



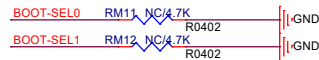
DDR3 16X1



FLASH



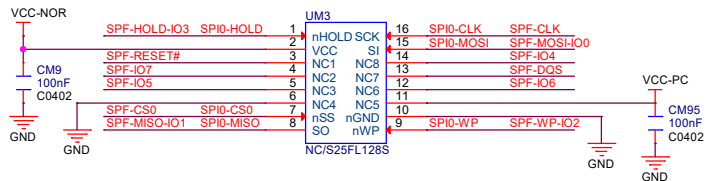
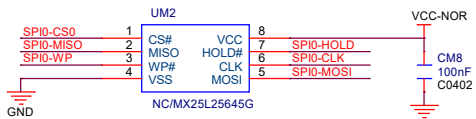
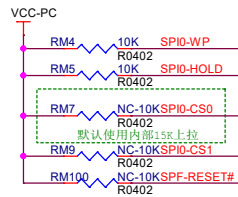
BOOT-SEL



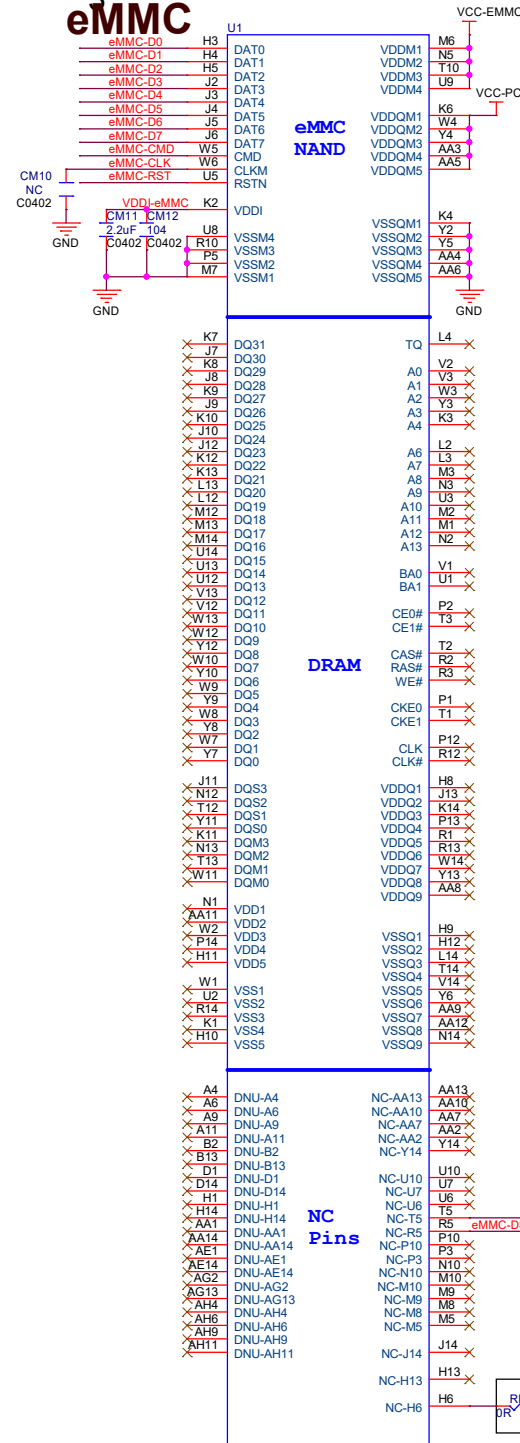
电阻靠近信号分叉点放置，以免影响信号质量

```
boot_sel[1:0]
0 0      SPI NAND->SPI NOR(4线)->SPI NOR(1线)>USB
0 1      SPI NOR(4线)->SPI NOR(1线)->SPI NAND->USB (快起)
1 0      SDC0->SPI NAND->SPI NOR(4线)->SPI NOR(1线)->UART BRUN->USB
1 1 (默认) SDC0->SPI NOR(4线)->SPI NOR(1线)->EMMC2_USR->EMMC2_BOOT->
        SPI0 NAND->UART BRUN->USB
```

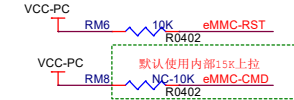
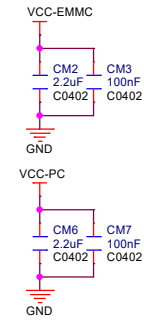
PC-SPI0



3 eMMC



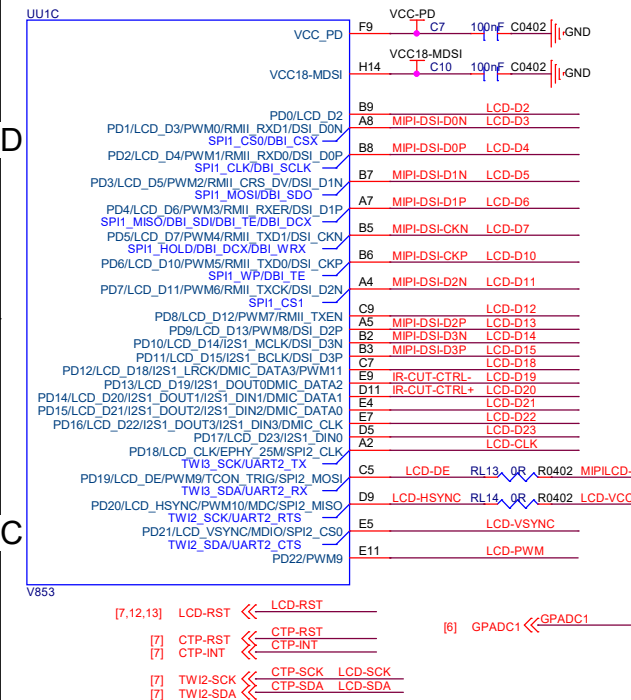
EMMC
BGA169-PT0_5-12X16H1A



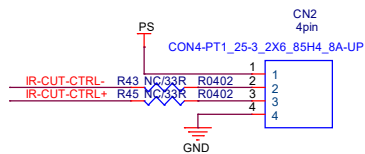
可以使用内部15K下拉,外部下拉默认NC
If use eMMC is not v5.0/v5.1,
then NC this resistor.

If eMMC is not v5.0/v5.1, then NC this two resistors.

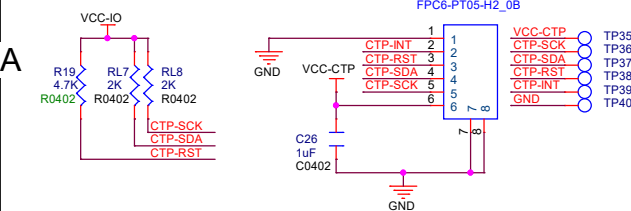
LCD/PWM



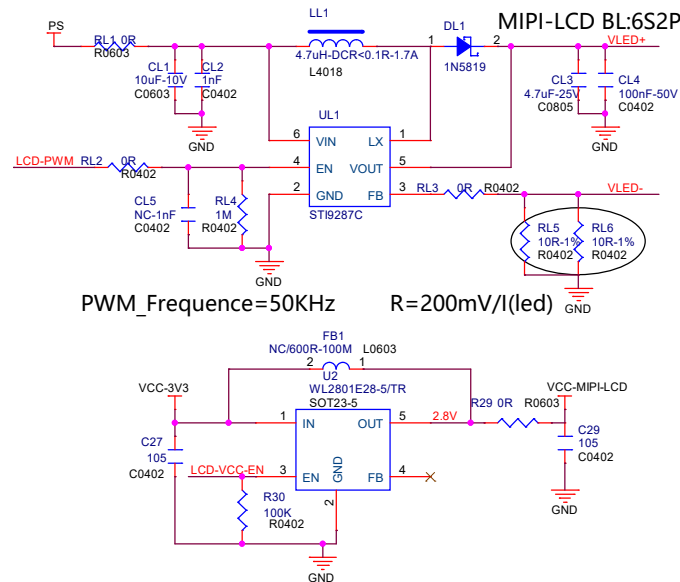
IR_Cut



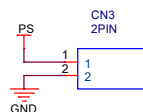
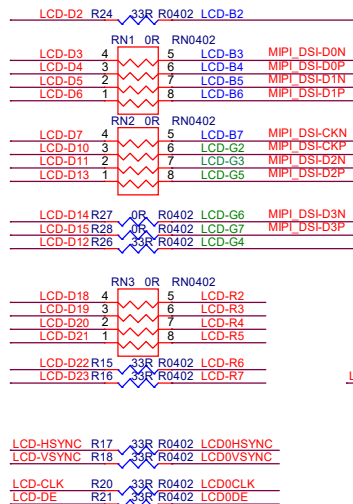
CTP CN



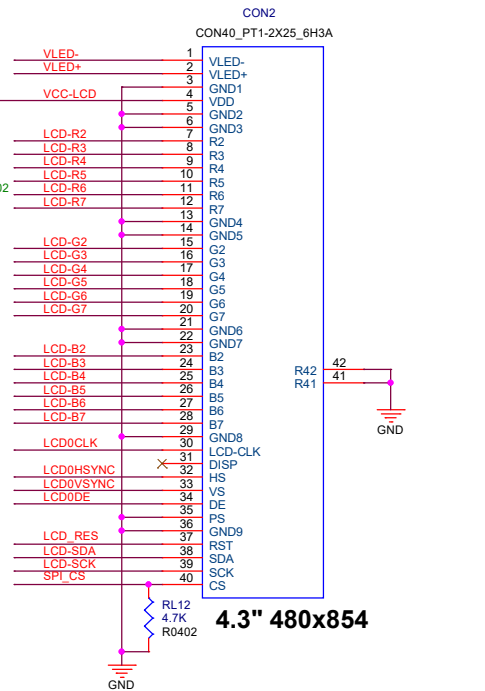
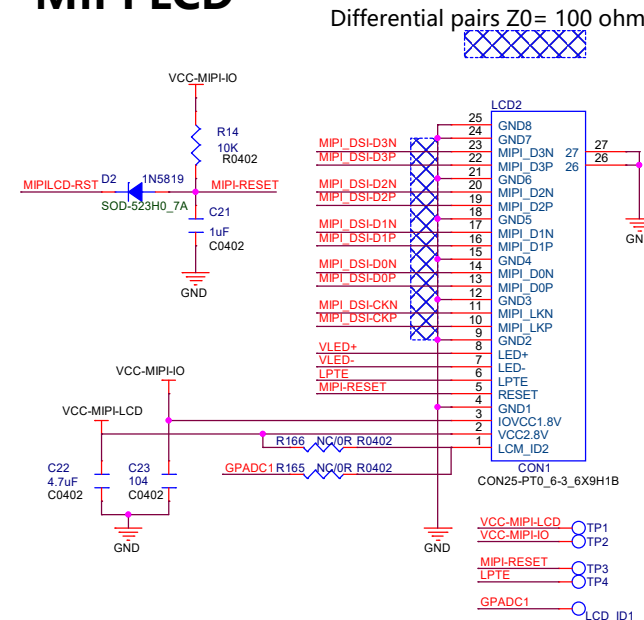
BACKLIGHT



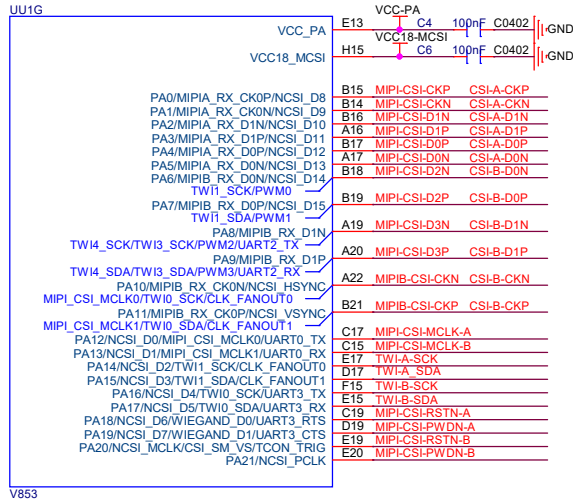
RGB LCD



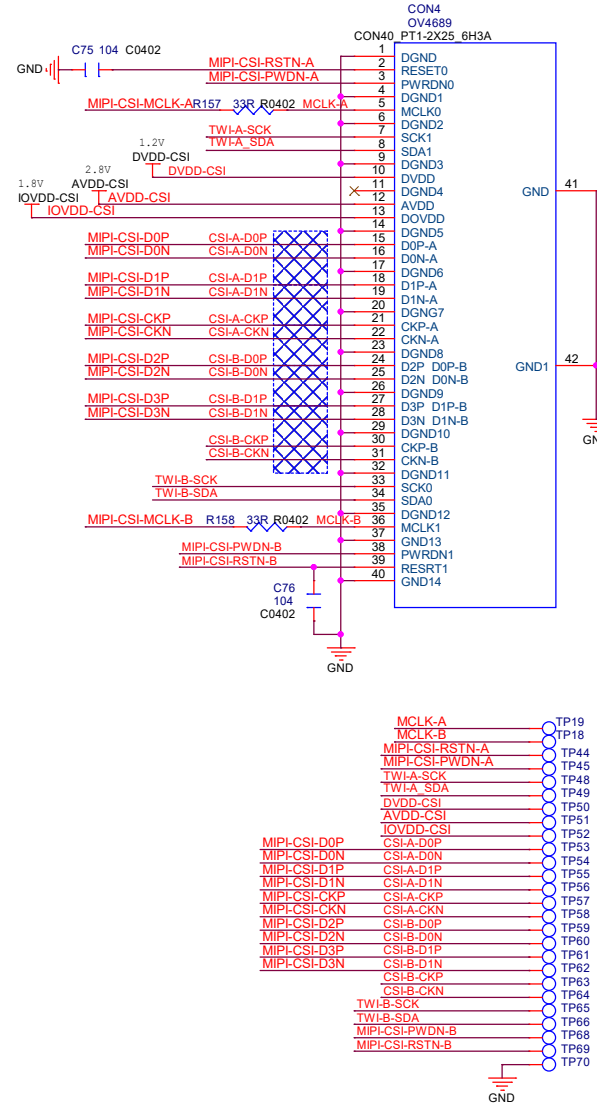
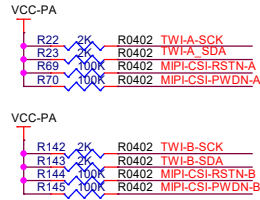
MIPI LCD



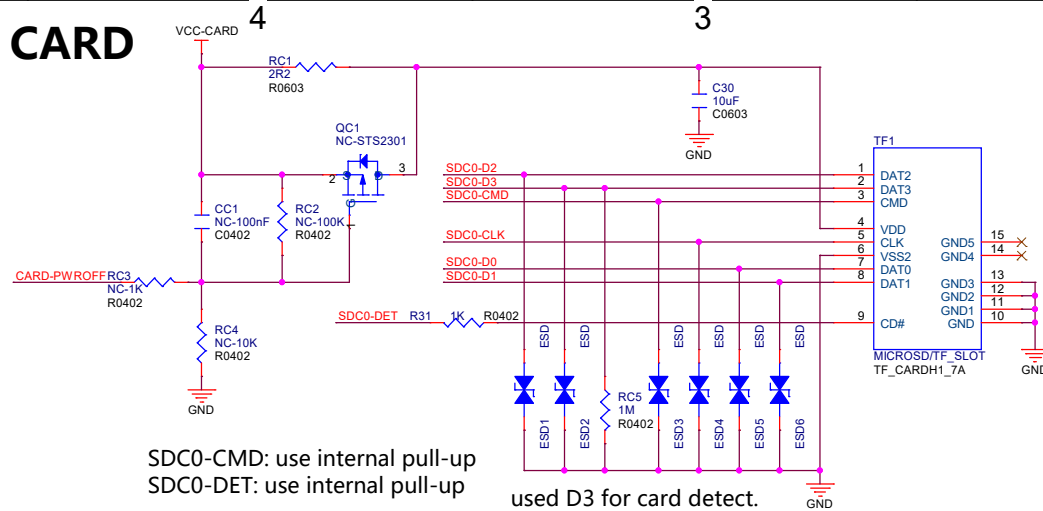
MIPI CSI



IO	两组MIPI-2lane	MIPI-4lane
PA0	MIPIA-CSI-CK0P	MIPI-CSI-CKP
PA1	MIPIA-CSI-CK0N	MIPI-CSI-CKN
PA2	MIPIA-CSI-D1N	MIPI-CSI-D1N
PA3	MIPIA-CSI-D1P	MIPI-CSI-D1P
PA4	MIPIA-CSI-D0P	MIPI-CSI-D0P
PA5	MIPIA-CSI-D0N	MIPI-CSI-D0N
PA6	MIPIB-CSI-D0N	MIPI-CSI-D2N
PA7	MIPIB-CSI-D0P	MIPI-CSI-D2P
PA8	MIPIB-CSI-D1P	MIPI-CSI-D3N
PA9	MIPIB-CSI-D1N	MIPI-CSI-D3P
PA10	MIPIB-CSI-CK0N	
PA11	MIPIB-CSI-CK0P	



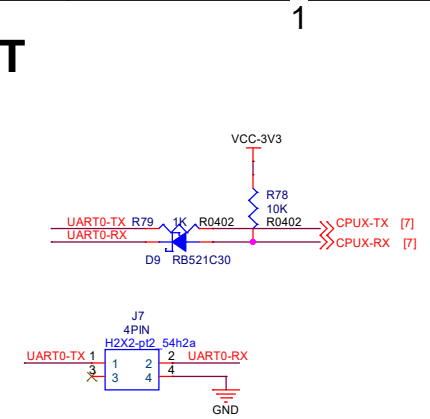
TF CARD



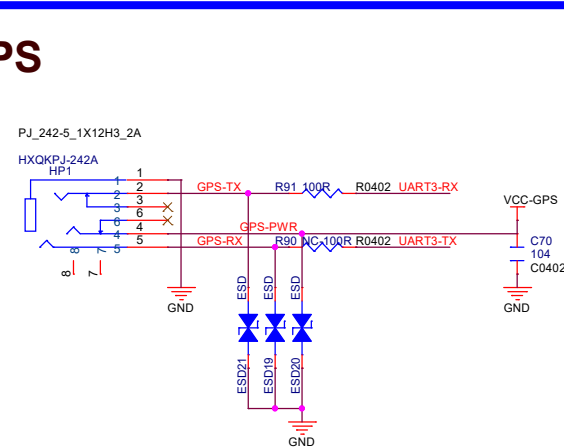
SDC0-CMD: use internal pull-up
SDC0-DET: use internal pull-up

used D3 for card detect.

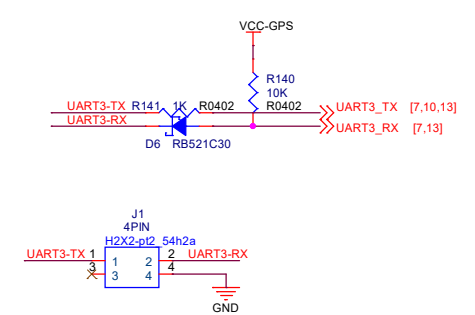
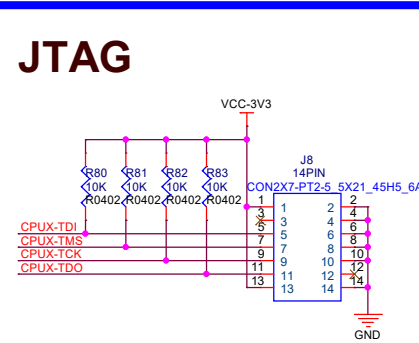
UART



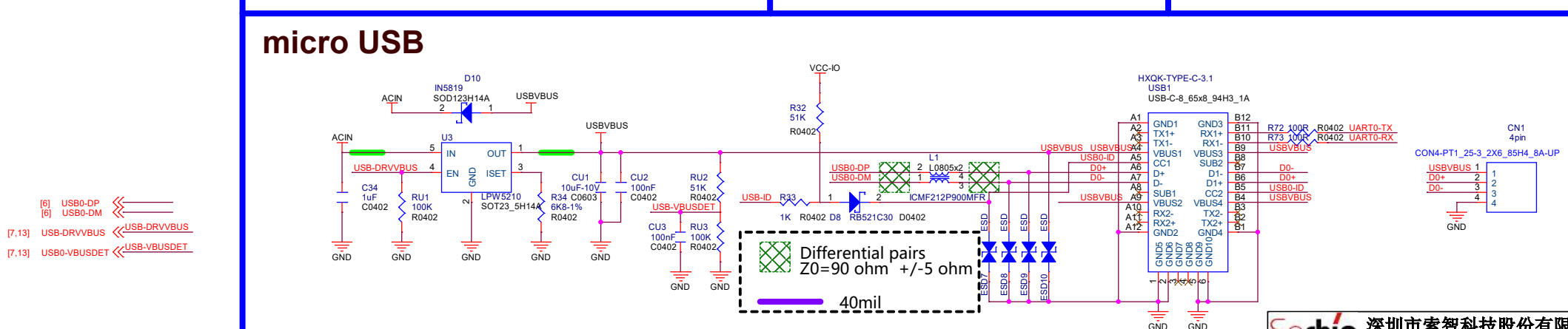
GPS



JTAG



micro USB



AUDIO

[6] MBIAS
[6] MICIN1P
[6] MICIN1N
[6] MICIN2P
[6] MICIN2N

[6] LINEOUTN
[6] LINEOUTP

[7,10] TWI2-SDA
[7,10] TWI2-SCK

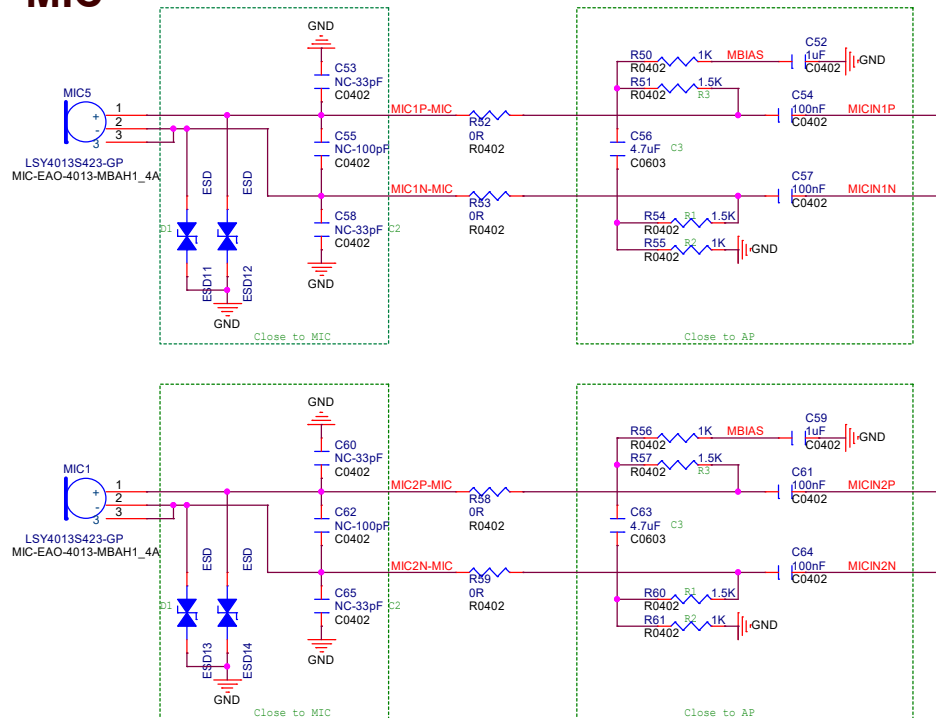
[7] PA-SHDN

[7,10,12] I2S0-MCLK
[7,12] I2S0-LRCK
[7,12] I2S0-BCLK
[7] I2S0-DIN
[7,12] I2S0-DOUT

I2S0-DOUT R9 NC-0R R0402 I2S0-DIN

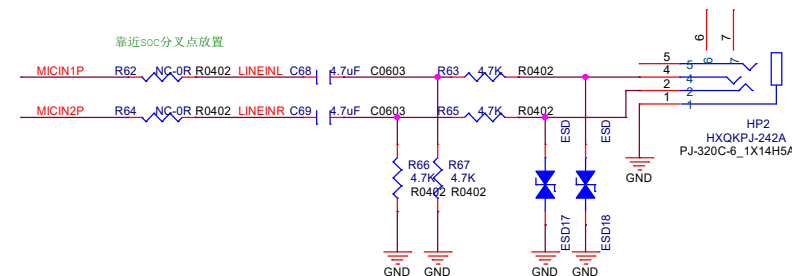
Need PCB MARK Signal Net

MIC

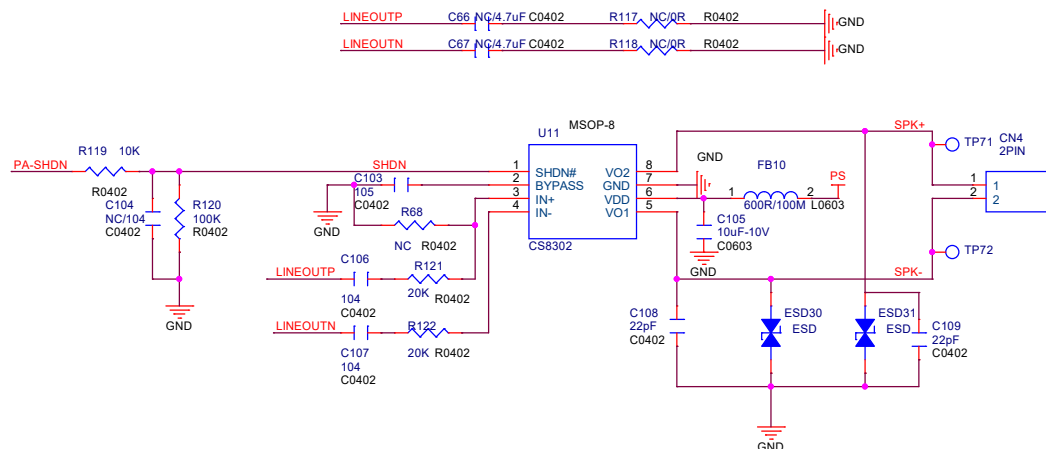


COMPONENT	Diferential	Single-ended
R1 R2 C3 D1	USE	NC
C2	NC-33pF	0R
R3	1.5K	1K

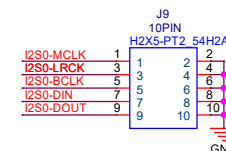
LINE IN



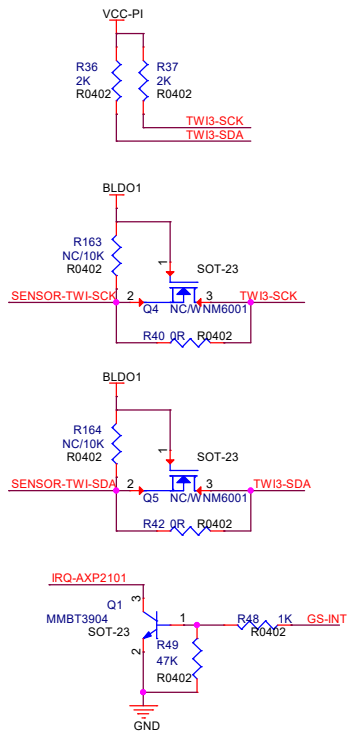
LINE OUT



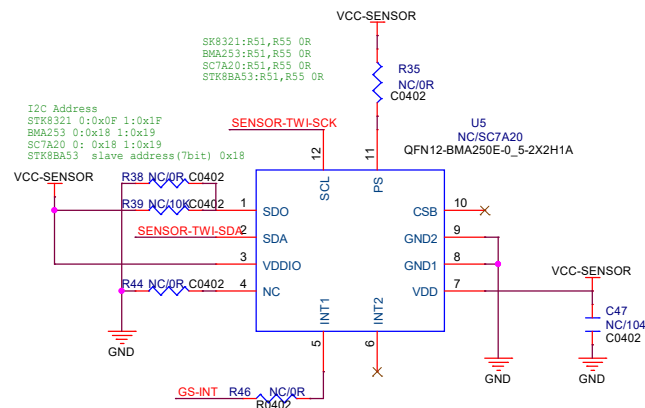
I2S



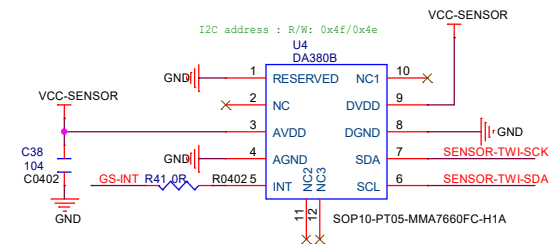
3axis G-sensor



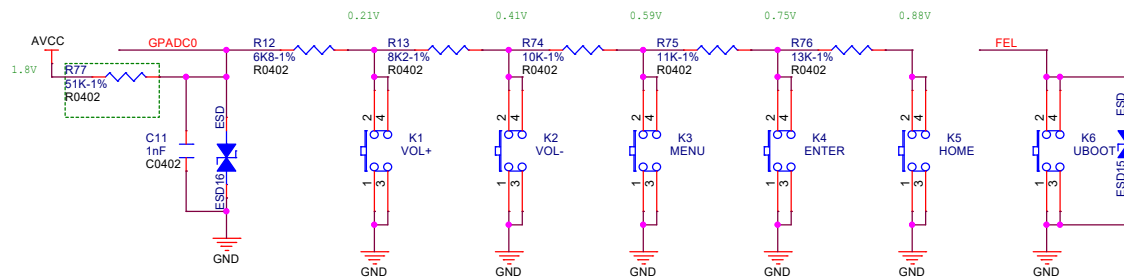
SC7A20



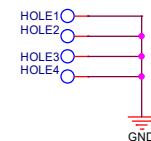
DA380B



KEY



SCREW HOLES





WIFI

[7] WL-SDIO-CMD
[7] WL-SDIO-CLK
[7] WL-SDIO-D0
[7] WL-SDIO-D1
[7] WL-SDIO-D2
[7] WL-SDIO-D3

[6] DCXO-RFCLK
[7] CLK-FANOUT0
[7] WL-WAKE-AP
[7] WL-PMU-EN-SOC

串口注意反向

[7] BT-UART-RX
[7] BT-UART-TX
[7] BT-UART-CTS
[7] BT-UART-RTS

[7] BT-WAKE-AP-SOC
[7] AP-WAKE-BT-SOC
[7] BT-RST-N-SOC

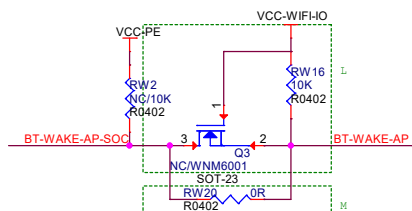
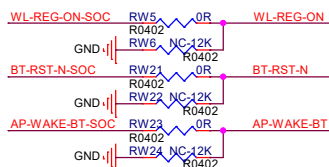
VCC33-WIFI
VCCIO-WIFI
VCC-WIFI
VCC-WIFIO

APXXX/XR819S

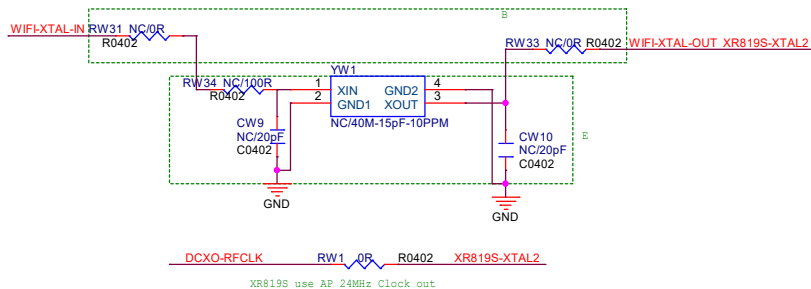
NOTE:
注意SOC端GPIO和WIFI-IO电平匹配

Retlak8919ftv
BL-M8189FS

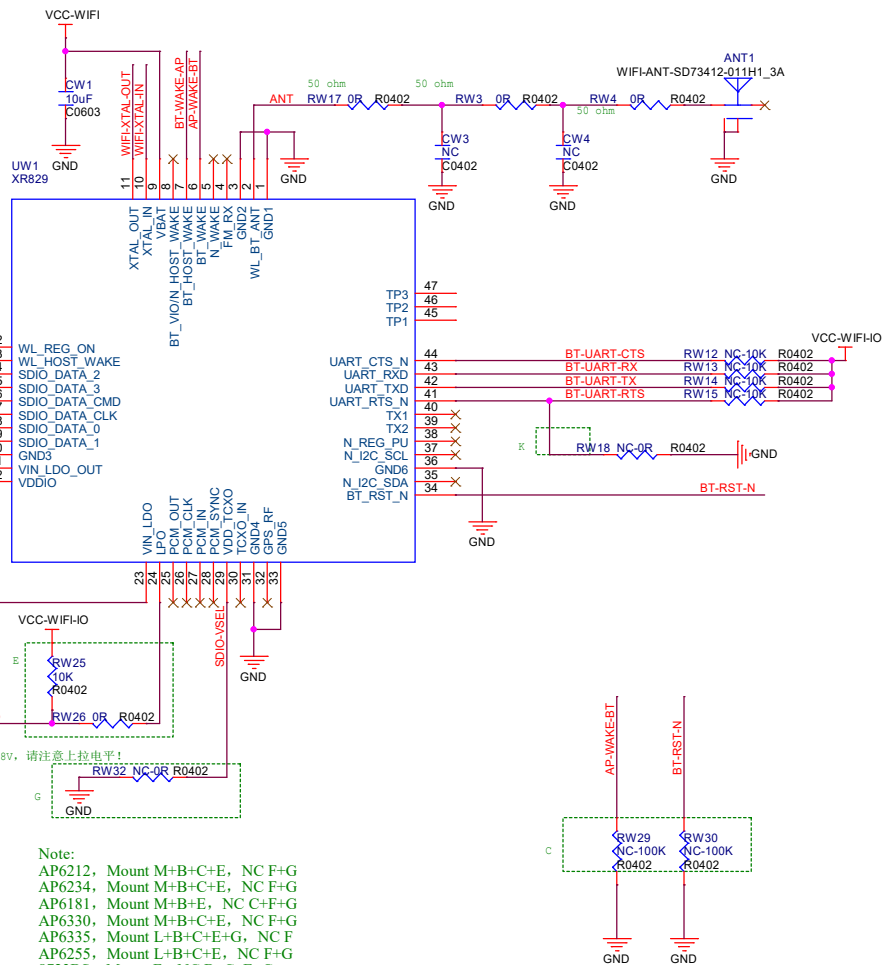
NOTE:3.3V 转1.8V 10K/12K电阻分压



Note:
AP6212/AP6234/AP6181/8723BS/AP6330, Y1=26M
AP6335/AP6255, Y1=37.4M



XR819S use AP 24MHz Clock out



Note:
AP6212, Mount M+B+C+E, NC F+G
AP6234, Mount M+B+C+E, NC F+G
AP6181, Mount M+B+E, NC C+F+G
AP6330, Mount M+B+C+E, NC F+G
AP6335, Mount L+B+C+E+G, NC F
AP6255, Mount L+B+C+E, NC F+G
8723BS, Mount F, NC B+C+E+G